AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning on Page 1, line 5 to line 16 with the following replacement paragraph:

The present application is related to the following commonly owned U.S. Patent Applications, incorporated in their entirety herein by reference: U.S. Patent Application No. 10/606,713 filed June 26, 2003 entitled "USE OF I²C PROGRAMMABLE CLOCK GENERATOR TO ENABLE FREQUENCY VARIATION UNDER BMC CONTROL," naming as inventors Naysen J. Robertson, Benjamin T. Percer, and Kirk Yates (Attorney Docket No.: 200208055-1); U.S. Patent Application No. 10/606,715 filed June 26, 2003 entitled "METHODS AND SYSTEMS FOR MASKING FAULTS IN A MARGIN TESTING ENVIRONMENT" naming as inventors Benjamin T. Percer and Naysen J. Roberston (Attorney Docket No.: 200312936-1); and U.S. Patent Application No. 10/606,714 filed June 26, 2003 entitled "METHOD AND CONSTRUCT FOR ENABLING PROGRAMMABLE, INTEGRATED SYSTEM MARGIN TESTING" naming as inventors Naysen J. Robertson, Benjamin T. Percer and Sachin N. Chheda (Attorney Docket No.: 200312936-1).